

VSS 500N12

VOLTAGE SURGE SIMULATOR



FOR TESTS ACCORDING TO ...

- › IEC 384-14
- › IEC 60060
- › IEC 664

VOLTAGE SURGE SIMULATION

The voltage surge simulator VSS 500N12 generates high voltage transients as required by several IEC standards. The voltage surge pulses are used to test the isolation (voltage withstand) capability of components, sockets, connectors, cables and many other items.

HIGHLIGHTS

- › SURGE VOLTAGE UP TO 12KV
- › WAVE FORM 1.2/50US
- › BUILT-IN SOURCE IMPEDANCE
- › INTERLOCK
- › WARNING LAMP CONTROL
- › VOLTAGE/CURRENT MEASUREMENT
- › SPARK-OVER DETECTION
- › MANUAL OPERATION
- › USB AND GPIB INTERFACE

APPLICATION AREAS



TECHNICAL DETAILS

VOLTAGE SURGE SIMULATOR, PULSE 1.2/50US

Voltage (o.c.)	500V - 12,000V ± 10%
Pulse front time	1.2us ± 30%
Pulse time to half value	50us ± 20%
Current (s.c.)	max. 24A ± 10%
Int. resistor	500ohm ± 10% Optional 40ohm or 50ohm
Polarity	Positive/negative/alternating
Event counter	1 - 30,000 or endless

TRIGGER

Automatic	Automatic pulse release
Manual	Single pulse release
External	External pulse release
CRO trigger	5V trigger signal for oscilloscope
Synchronisation	0° - 360°, resolution 1°
Repetition rate	max. 1Hz (1s - 999s)

OUTPUT

Direct	Via HV connector; Zi = 500ohm or as per selected option (40ohm or 50ohm)
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MEASUREMENTS

CRO Ū-monitor	10Vp at 12,000V
CRO Î-monitor	10Vp at 24A
Peak voltage	12,000V in the LCD display
Peak current	24A in the LCD display
Current limiter	Resolution 0.1A for Ri = 500ohm Resolution 1.0A for Ri = 40ohm

TEST ROUTINES

Quick Start	Immediate start; easy-to-use and fast
User Test routines	Change Polarity after n pulses Change voltage after n pulses
Service	Service, setup, self test

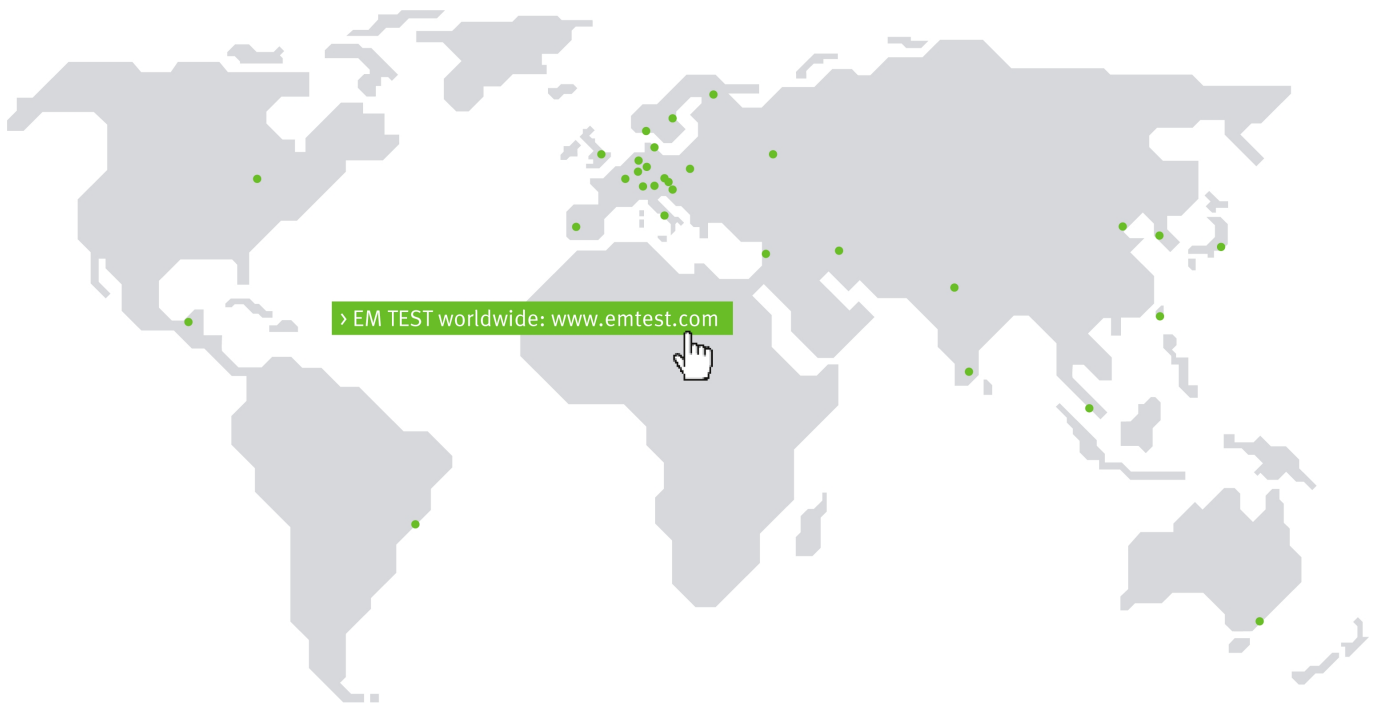
INTERFACE

Serial interface	USB
Parallel interface	IEEE 488, addresses 1 - 30

GENERAL DATA

Dimensions, weight	19"/3HU, approx. 15kg
Supply voltage	115/230V +10/-15%
Fuses	2 x T 2AT (230V) or 2 x T4AT (115V)

COMPETENCE WHEREVER YOU ARE



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Information about scope of delivery, visual design and technical data correspond with the state of development at time of release.
Technical data subject to change without further notice.